



JPW

PATENT  
Docket No. 20063/OG03-036

**IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE**

Applicant(s): ) I hereby certify that this paper and the  
) documents referred to as enclosed  
**Jung et al.** ) therewith are being deposited with the  
) United States Postal Service as first  
U.S. Serial No.: 10/749,648 ) class mail, postage prepaid, in an  
) envelope addressed to Commissioner  
For: "Flash Memory with Reduced ) for Patents, P.O. Box 1450,  
Source Resistance and Fabrication ) Alexandria, Virginia 22313-1450 on  
Method" ) this date:  
)  
Filed: 12/30/2003 ) June 21, 2005  
) Date  
Group Art Unit: 1756 )  
)  
Examiner: Not Yet Assigned ) Mark G. Hanley  
) Mark G. Hanley  
) Attorney for Applicant(s)  
) Registration No. 44,736

**STATUS LETTER**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Kindly advise when an Office action can be expected in the above-referenced  
matter.

Respectfully submitted,

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6/21/05  
Date

By: Mark G. Hanley  
Mark G. Hanley  
Registration No.: 44,736